PATENT APPLICATION SERIAL NO. 10/759,936

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Chaudhry et al.

88888888 Serial Number: 10/759,936 Group Art Unit: 2128

Filed: January 16, 2004

Examiner: Herng Der Day For: METHOD AND APPARATUS FOR VLSI

CLOCK GATED POWER ESTIMATION USING LCB COUNTS

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

## RESPONSE

In response to the Office Action mailed December 5, 2006 ("Office Action"), Applicants respond as follows: